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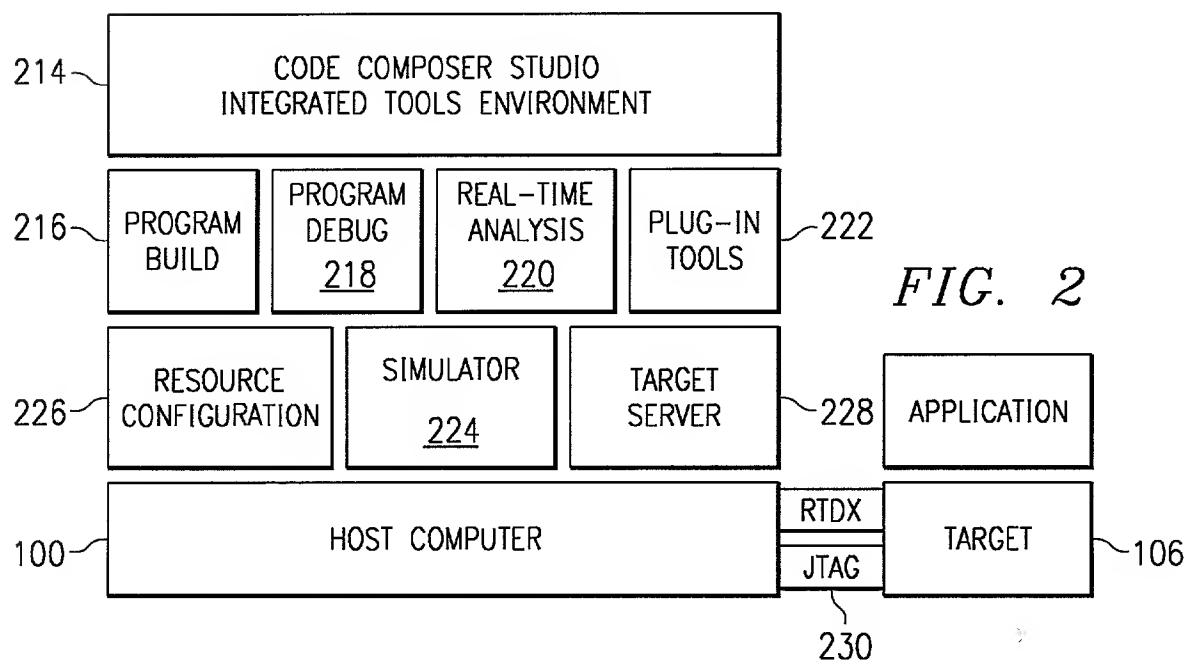
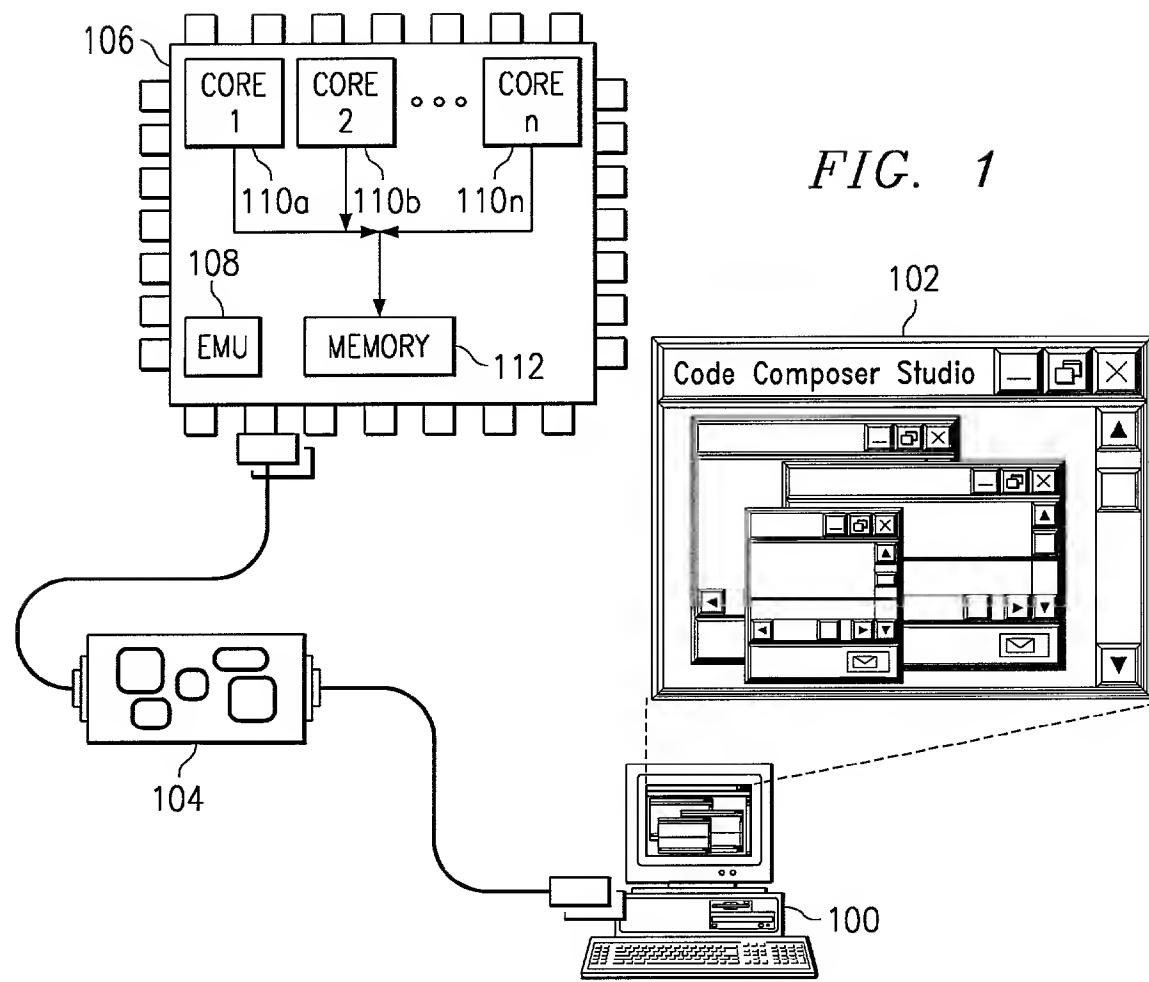
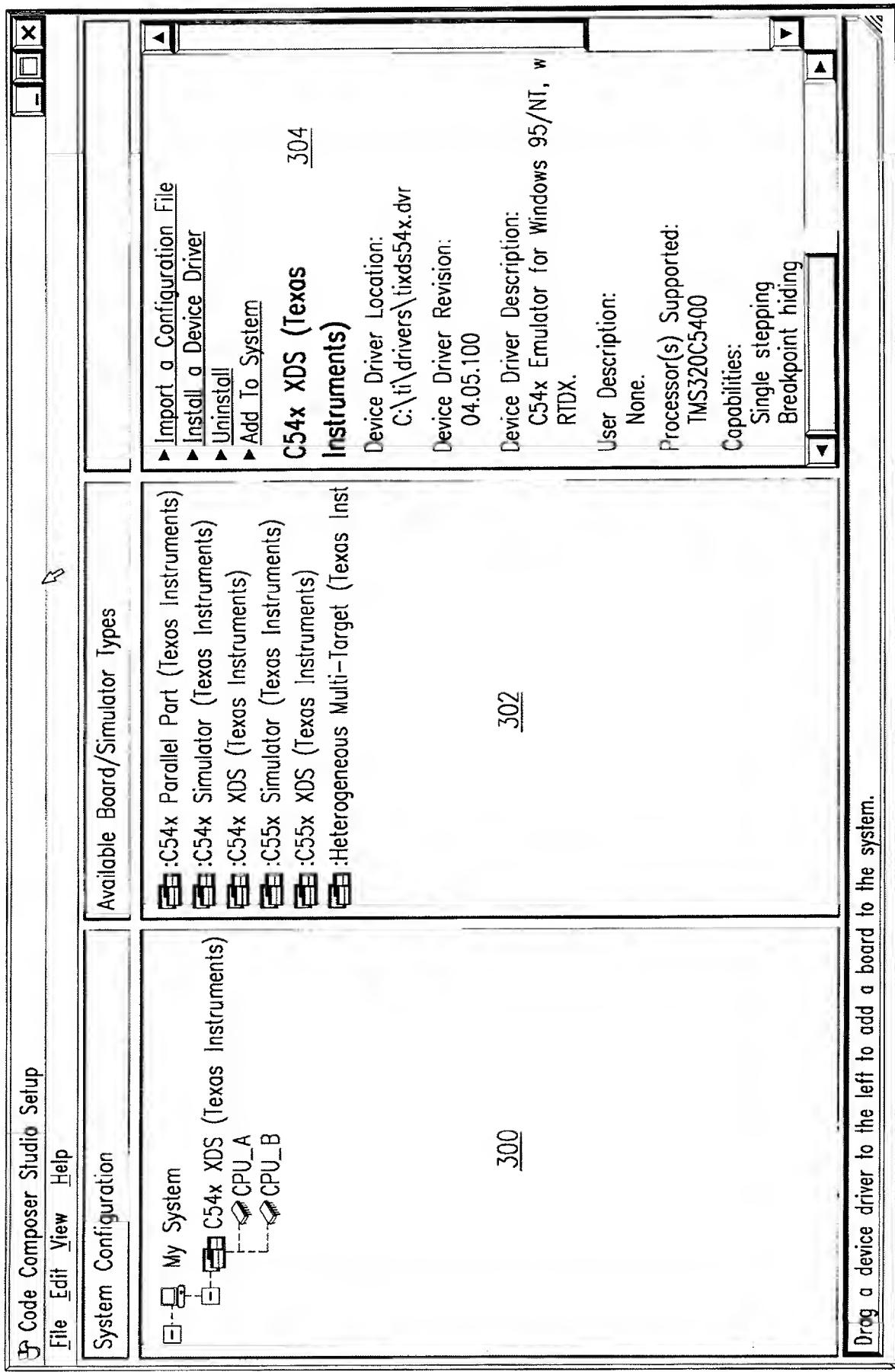


FIG. 3



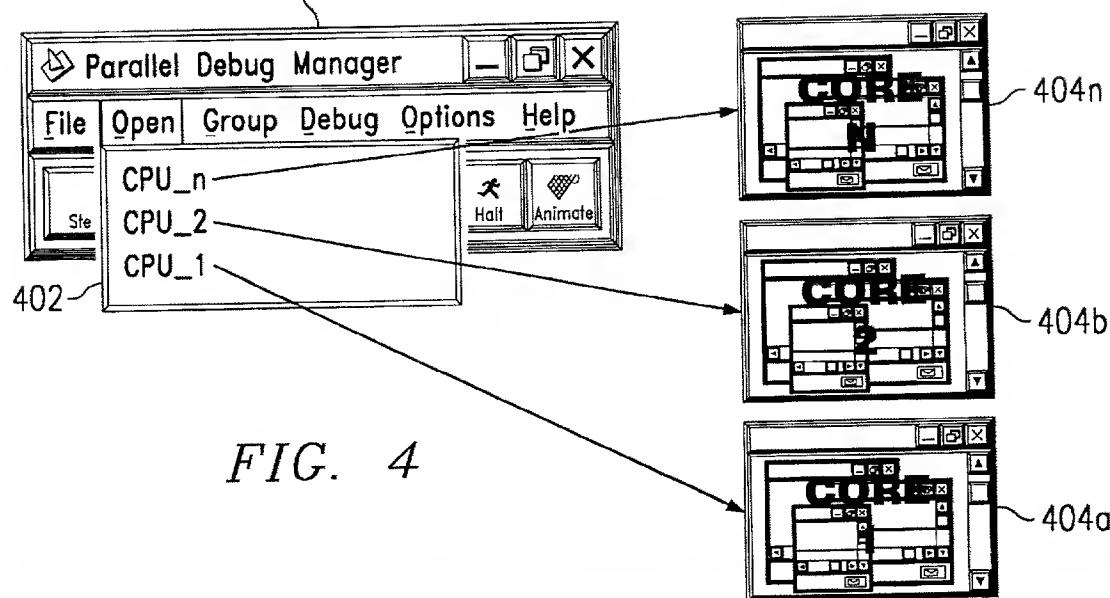


FIG. 4

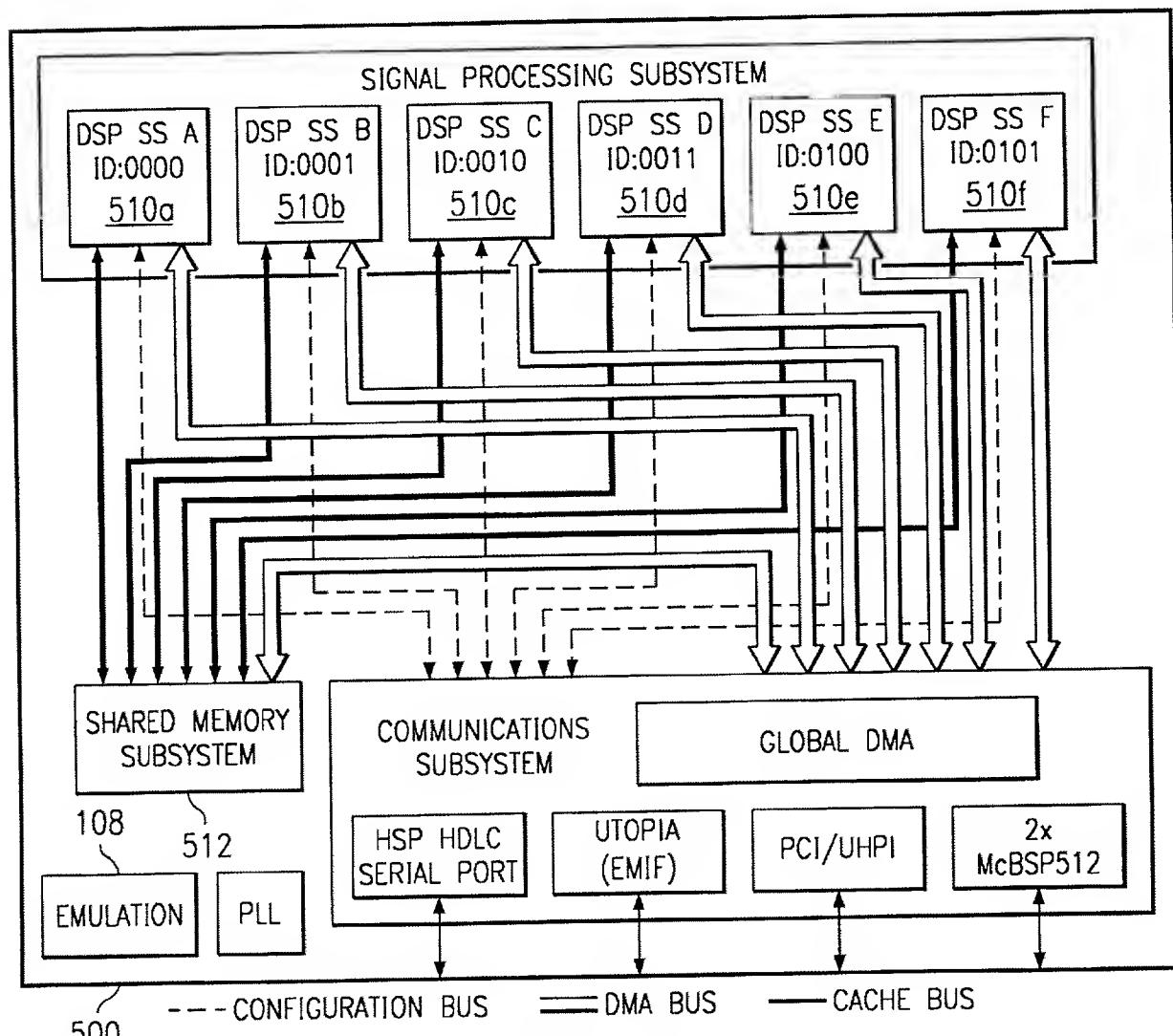


FIG. 5

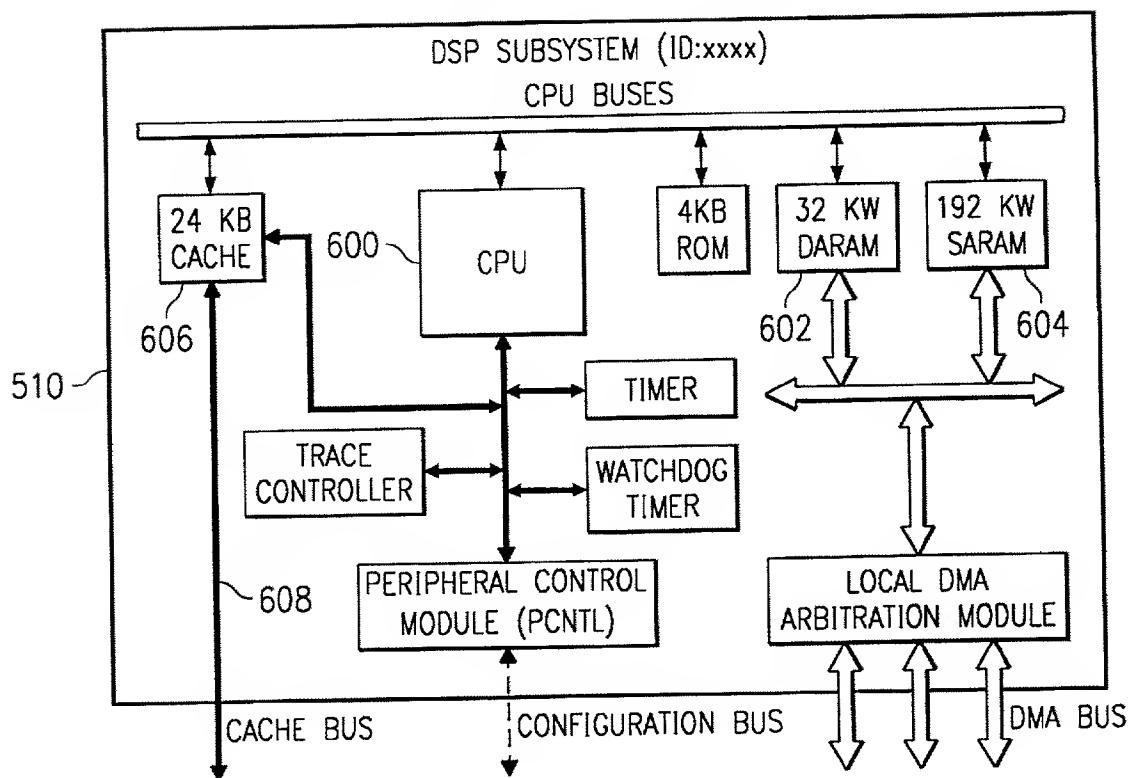
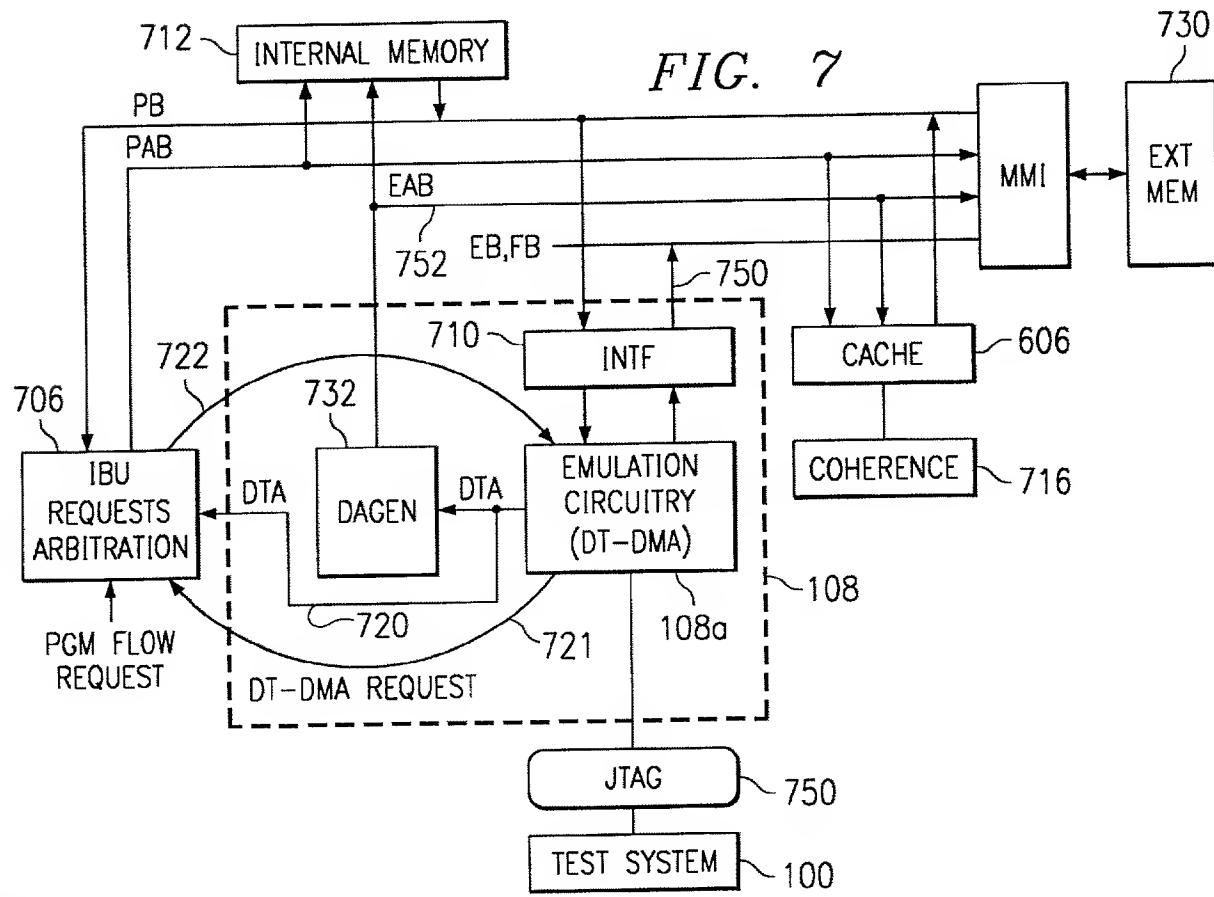


FIG. 7



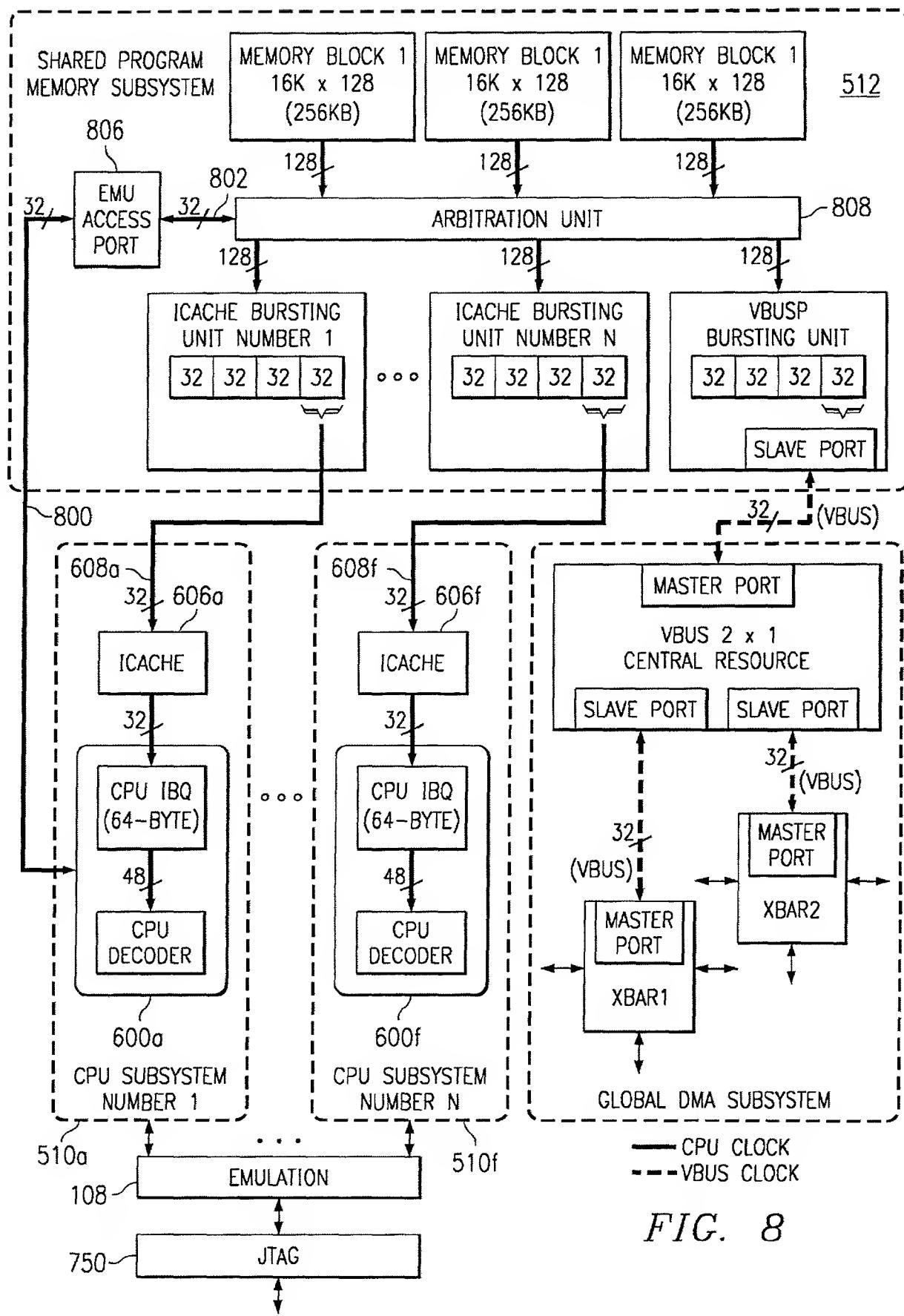
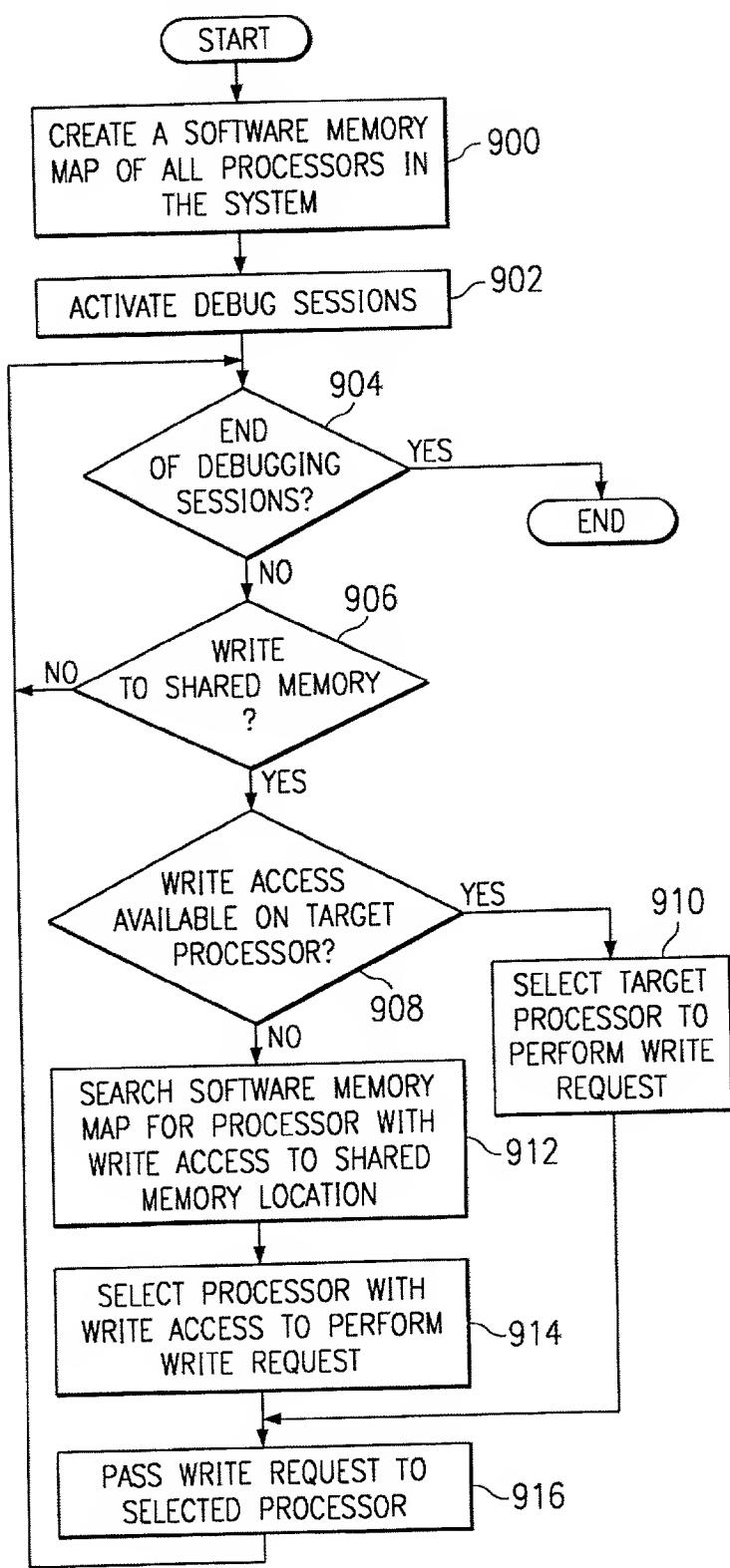


FIG. 8

FIG. 9



BYTE ADDRESS

000000h	LOCAL MEMORY MAPPED REGISTERS
00009Fh	LOCAL DARAM (64KBYTE) DATA
0000A0h	
00FFFFh	LOCAL SARAM (64KBYTE) DATA/PROG
010000h	
01FFFFh	LOCAL SARAM (320KBYTE) DATA
020000h	
06FFFFh	RESERVED
070000h	
F3FFFFh	SHARED SARAM (768KBYTE) PROG
F40000h	
1000	LOCAL ROM (4KBYTE)
FFF000h	
FFFFFFFFFFh	

FIG. 10

FIG. 11

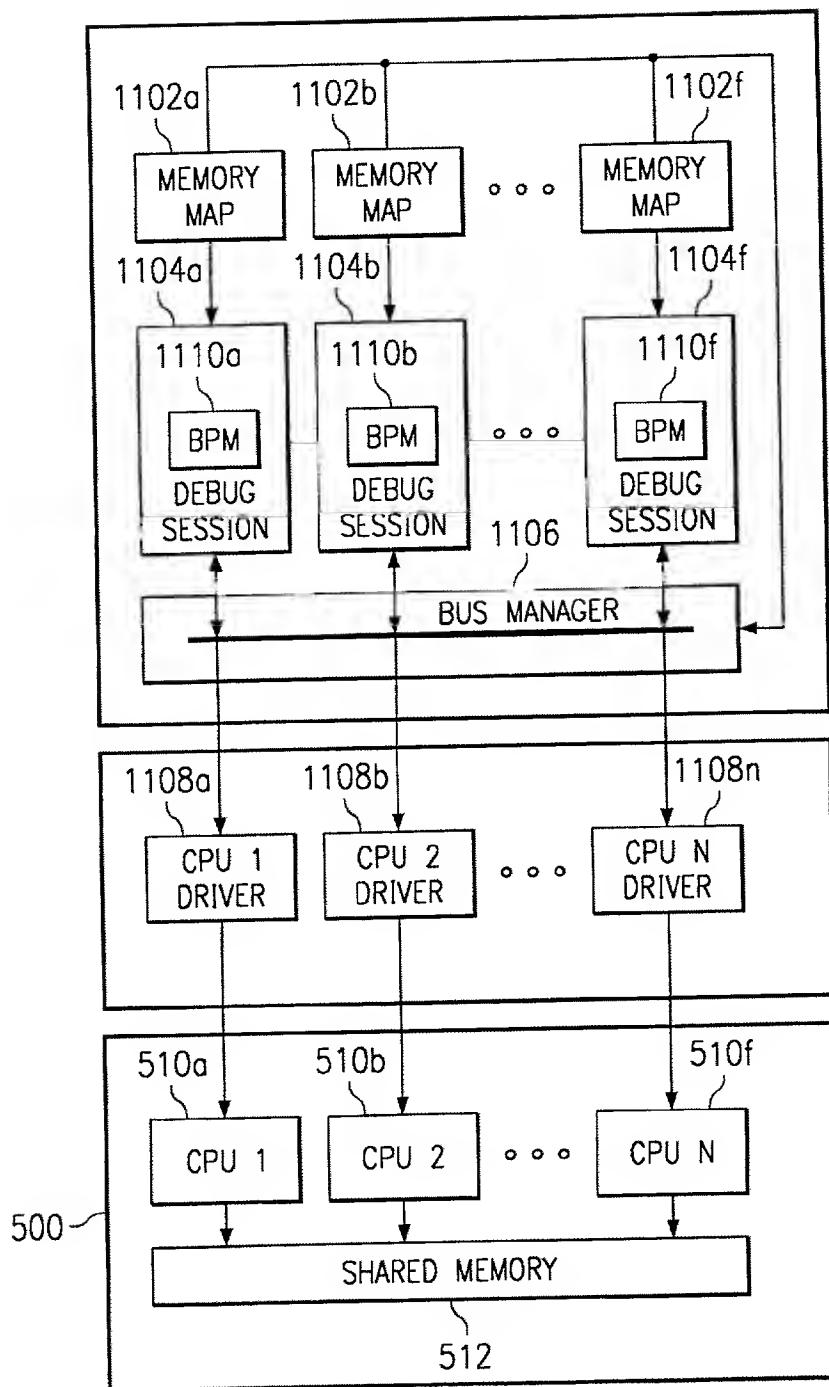


FIG. 12A

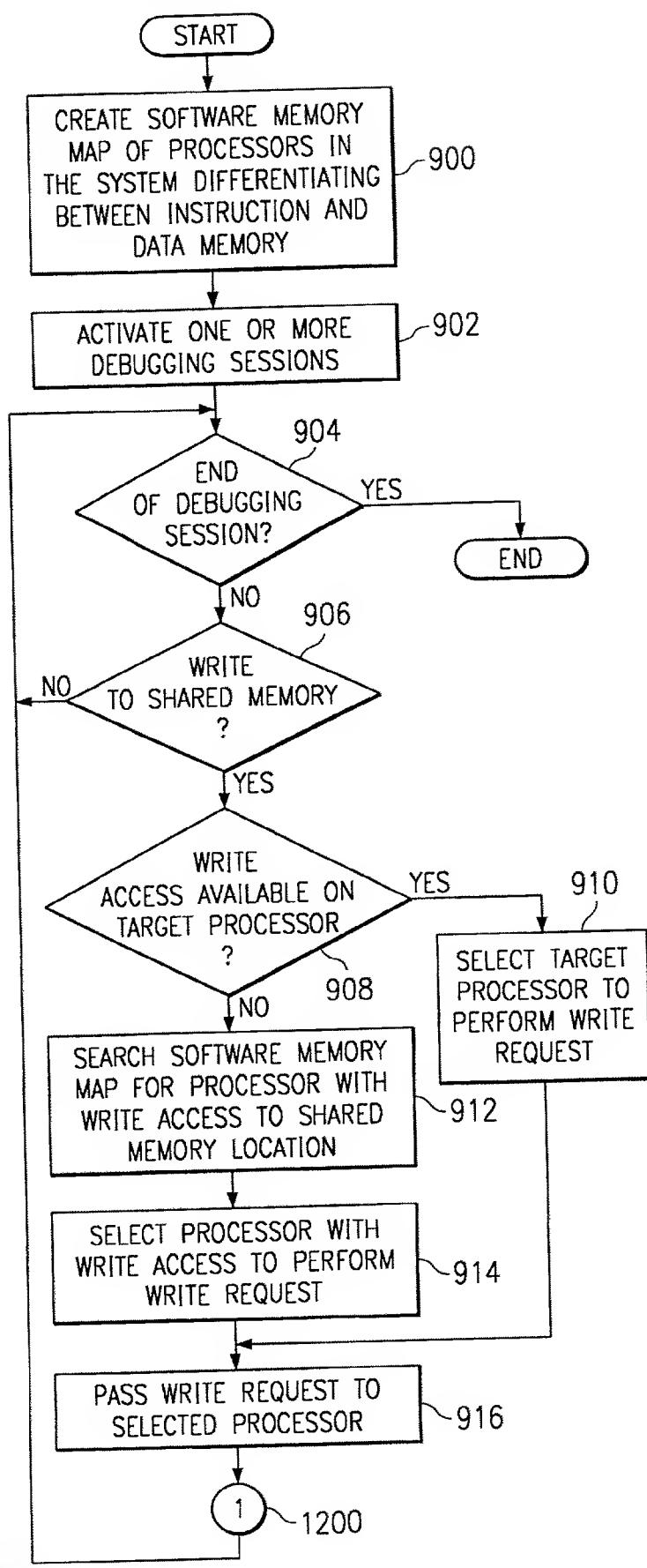


FIG. 12B

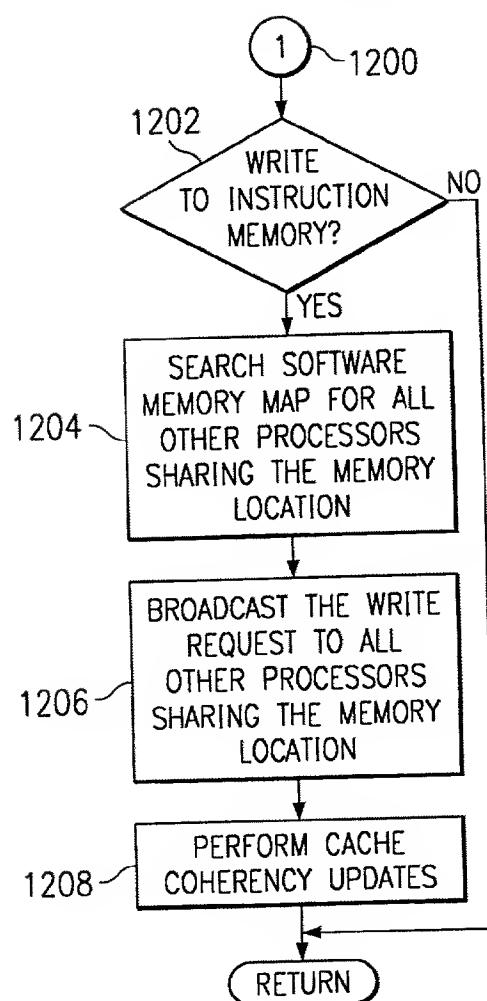


FIG. 13A

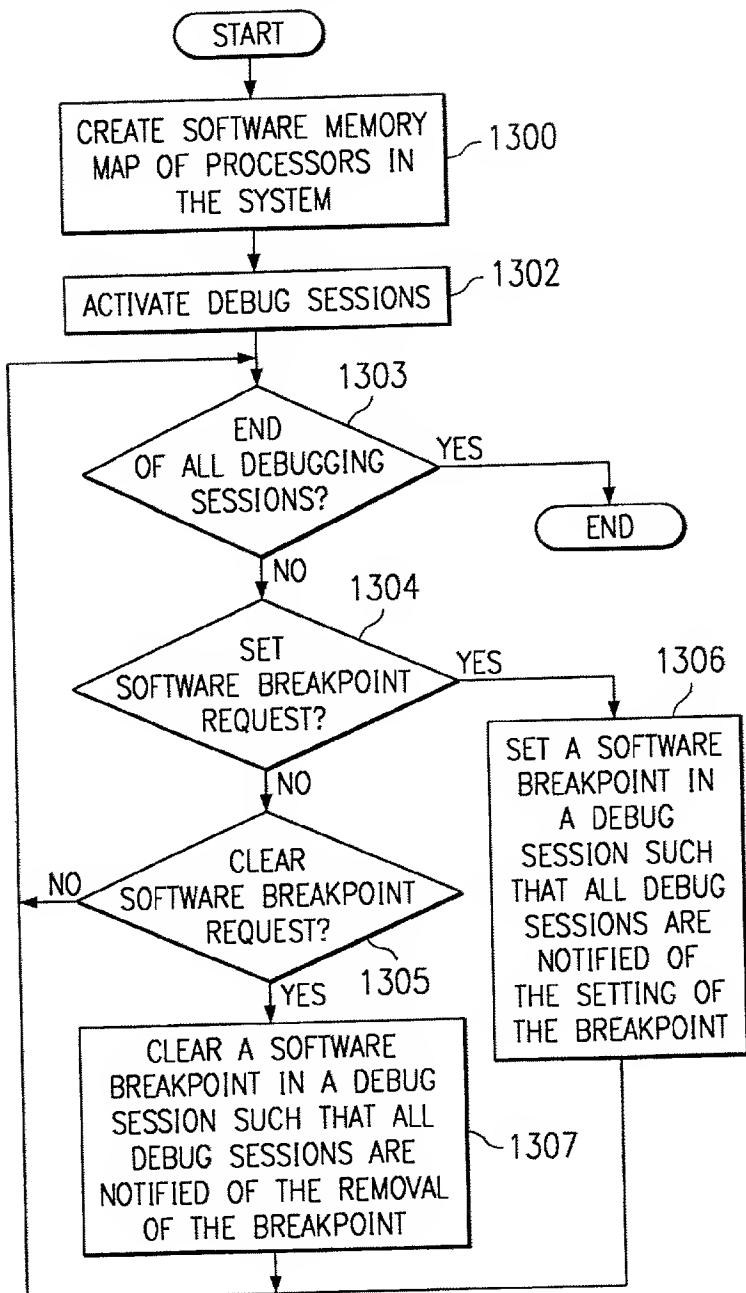
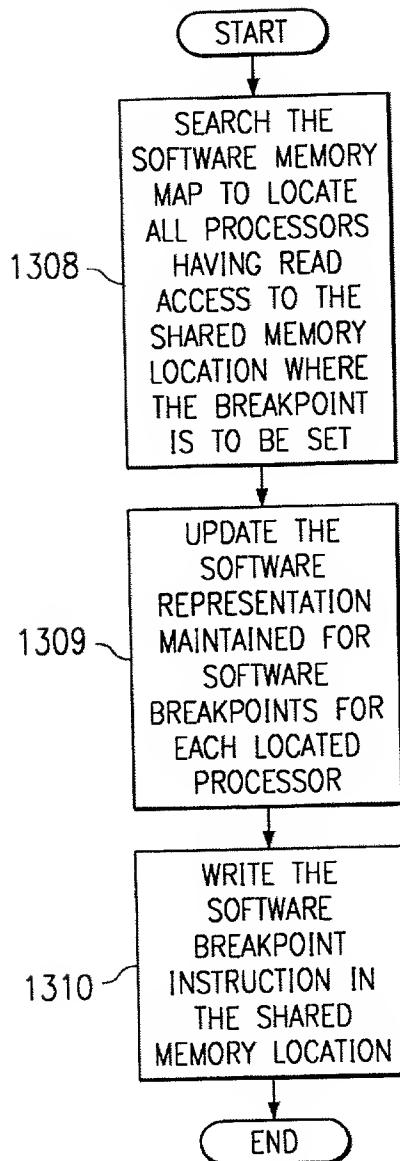


FIG. 13B



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FIG. 14

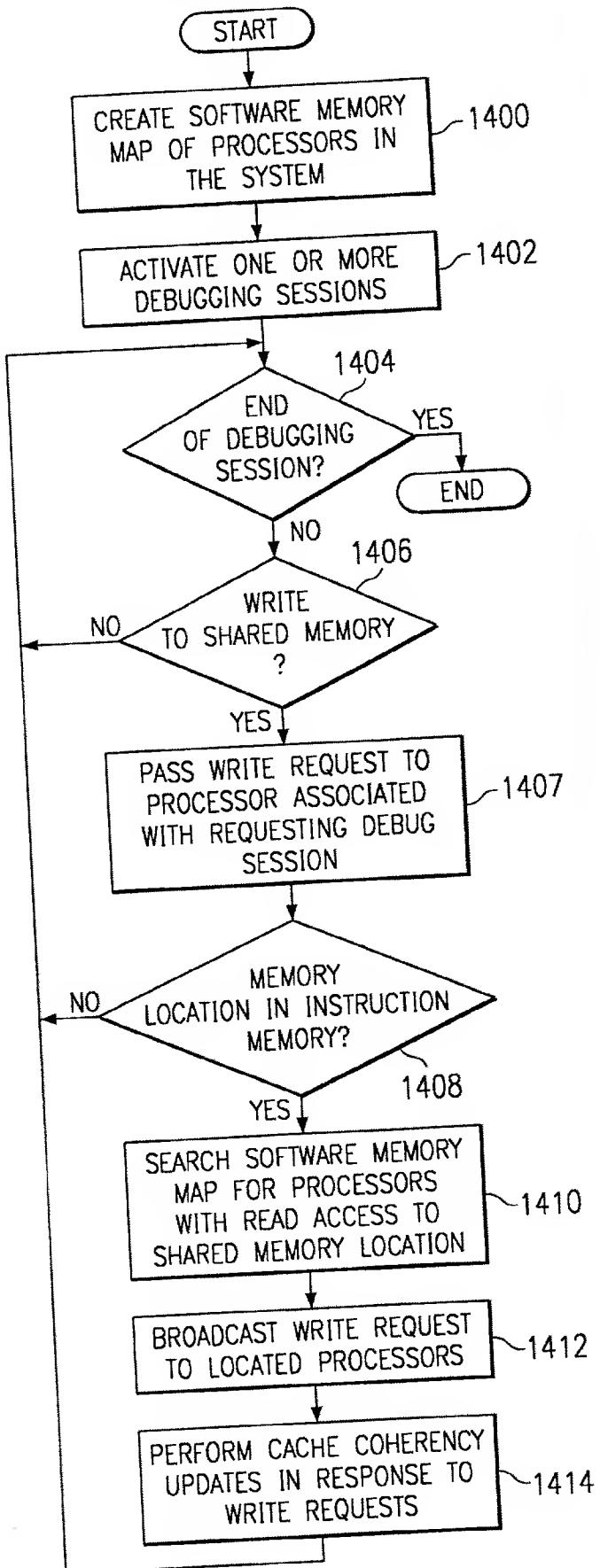
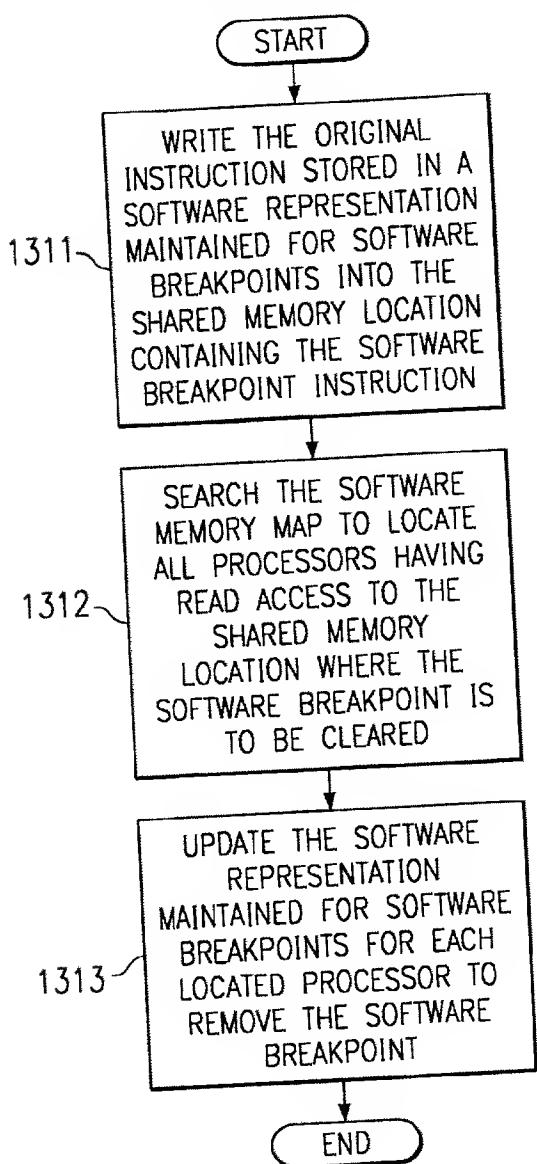


FIG. 13C



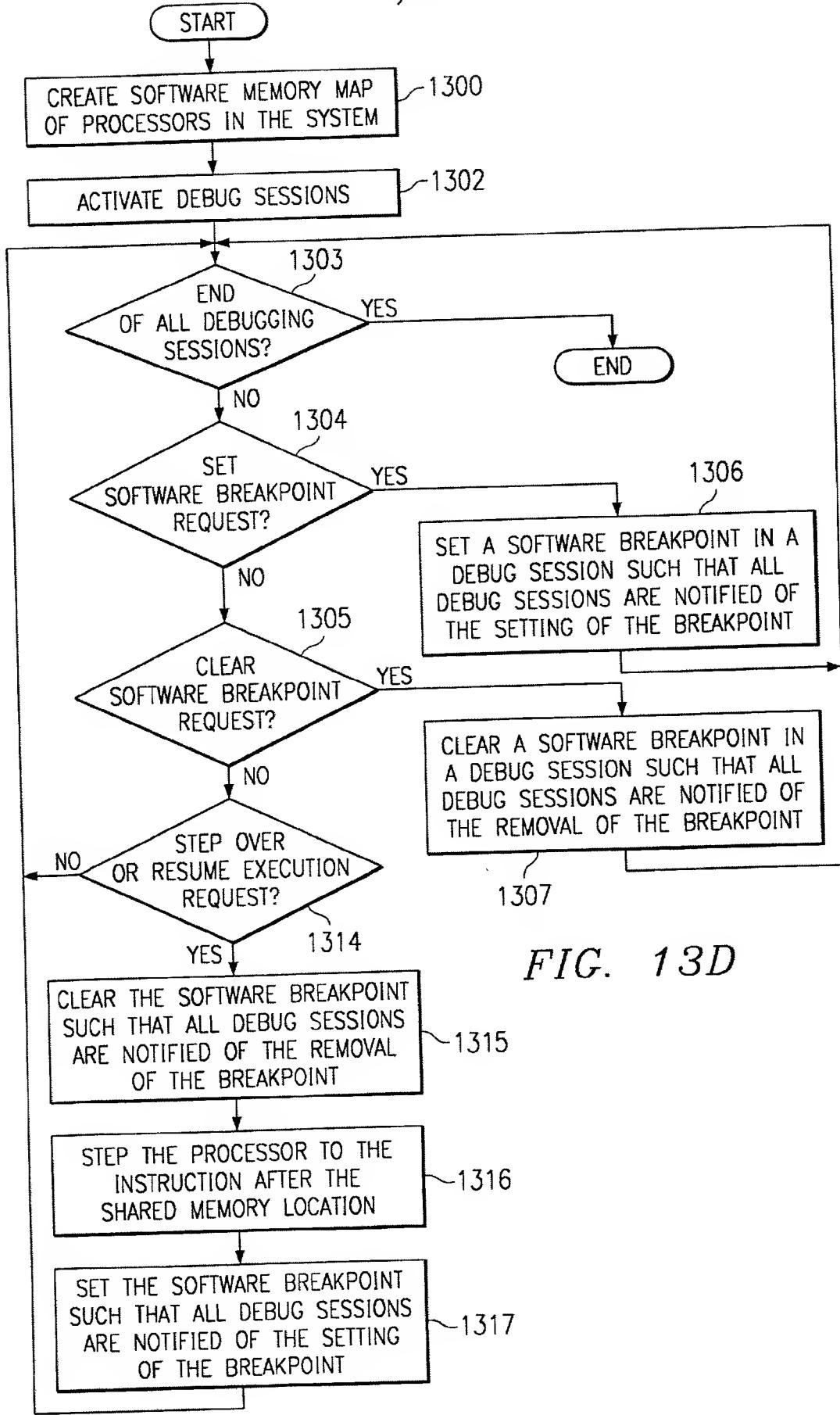


FIG. 13D

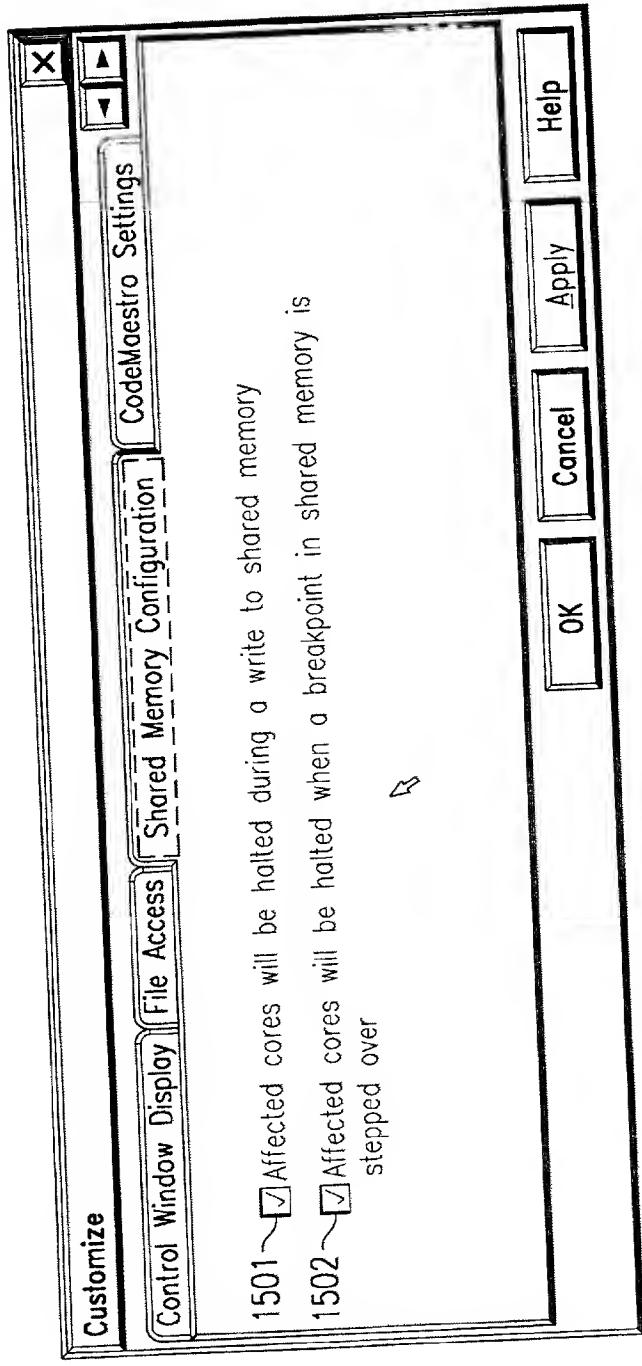


FIG. 15

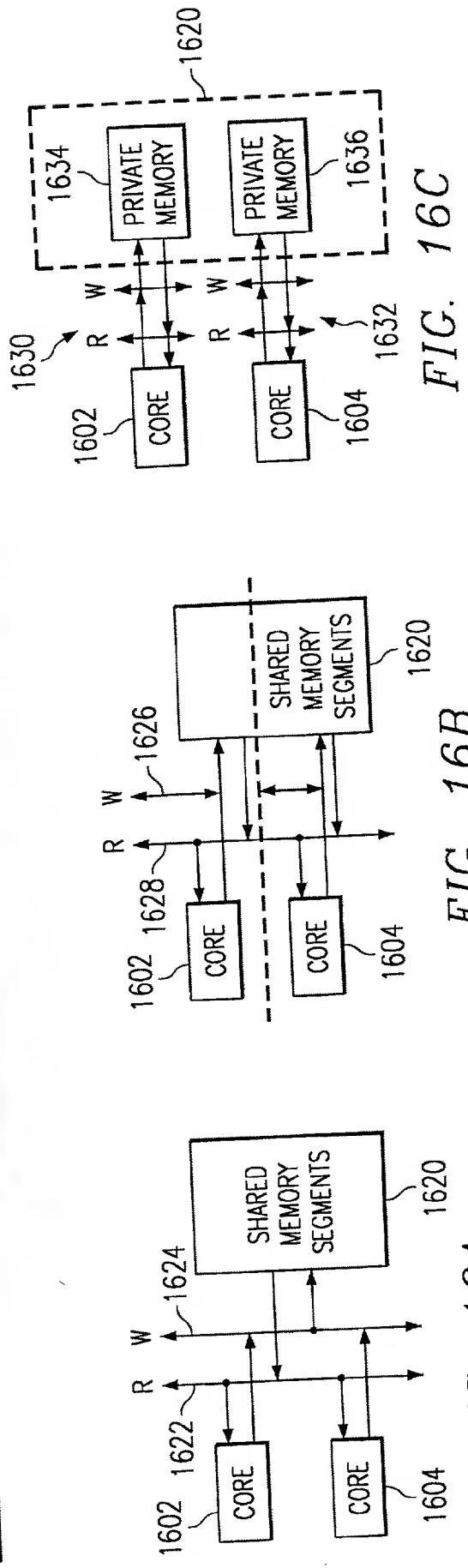


FIG. 16A

FIG. 16B

FIG. 16C

FIG. 17

